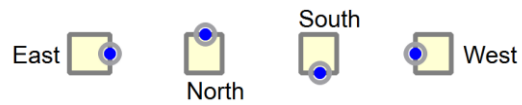


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## Output Gate



An output gate continuously displays results during a simulation.

Property	Settings	Meaning
<b>Label</b>	Standard	A unique name is recommended but is mandatory for viewing in a waveform window.
<b>Orientation</b>	Standard	Defines the position of the input port
<b>Data Bits</b>	Standard	Bit width of output value
<b>Base</b>		Bit: up to 32 bits are shown Hex: displays up to 8 hexadecimal digits Unsigned: Output bits are interpreted as a dual number. The corresponding value is displayed using decimal digits. Signed: A two's complement representation is shown using an optional minus sign and decimal digits.