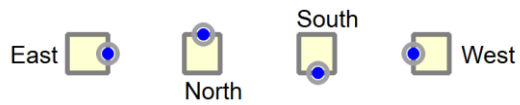


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## Monitor Gate



Monitor gates are useful for inspecting internal signal values.

A monitor gate continuously displays values during a simulation. In contrast to output gates monitor gates do not contribute to a circuit's interface of input and output gates with fixed bit widths.

Hence, a monitor gate gets its data width automatically set when connected to signal wire.

Property	Settings	Meaning
<b>Label</b>	Standard	A unique name is recommended but is mandatory for viewing in a waveform window.
<b>Orientation</b>	Standard	Defines the position of the input port
<b>Base</b>		<div>Bit: up to 32 bits are shown</div> <div>Hex: displays up to 8 hexadecimal digits</div> <div>Unsigned: Bits are interpreted as a dual number. The corresponding value is displayed using decimal digits.</div> <div>Signed: A two's complement representation is shown using an optional minus sign and decimal digits.</div>